

Fig. 1

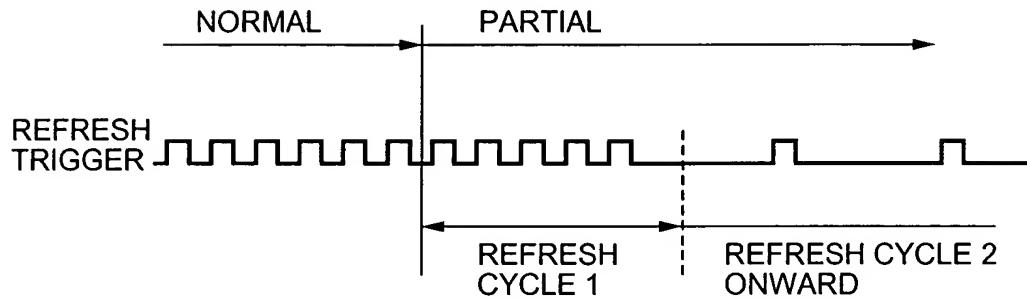


Fig. 2

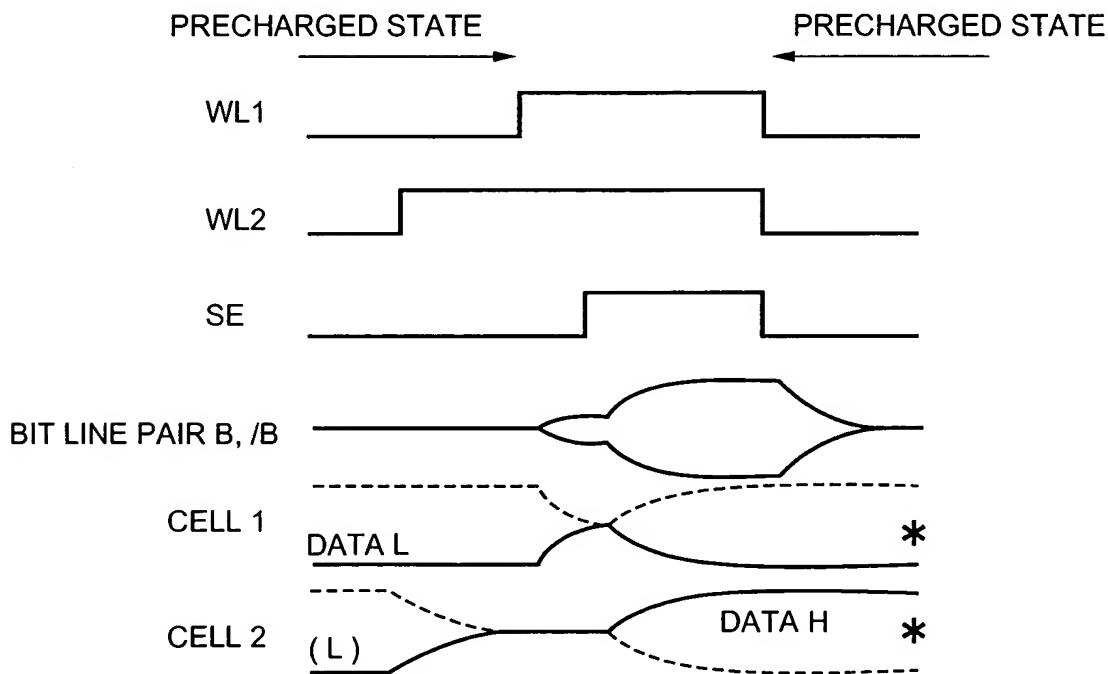


Fig. 3

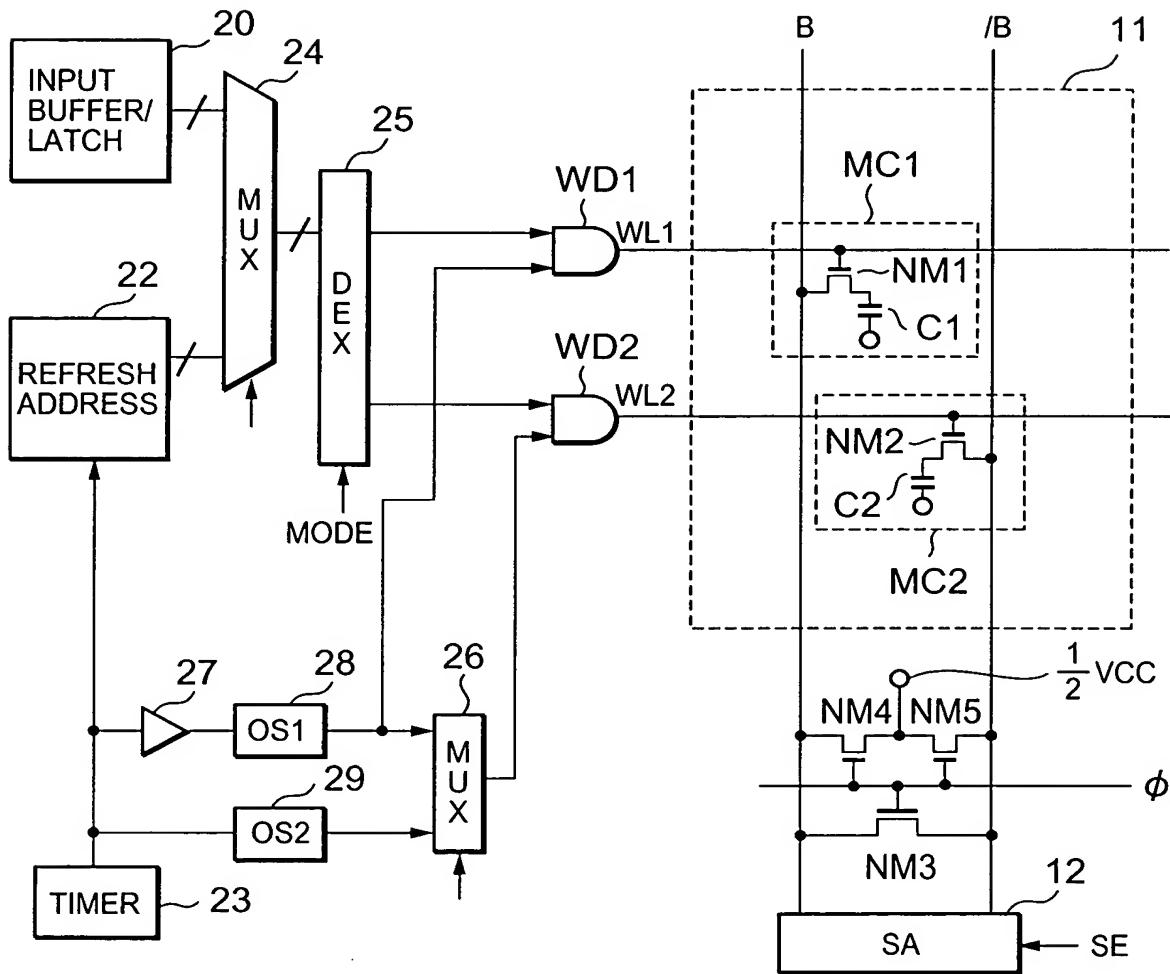


Fig. 4A

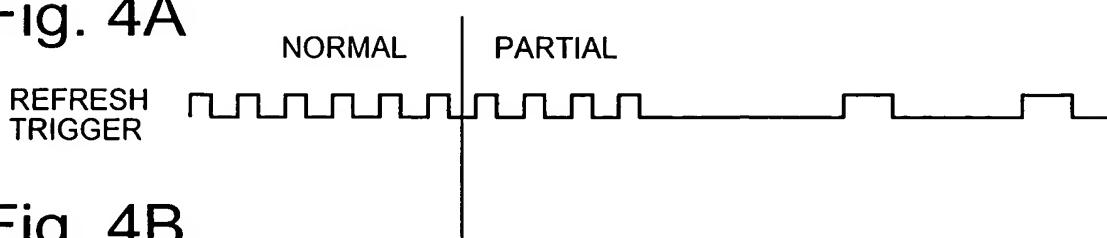


Fig. 4B

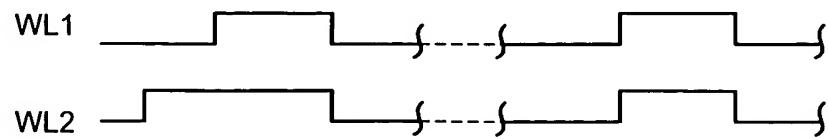


Fig. 5

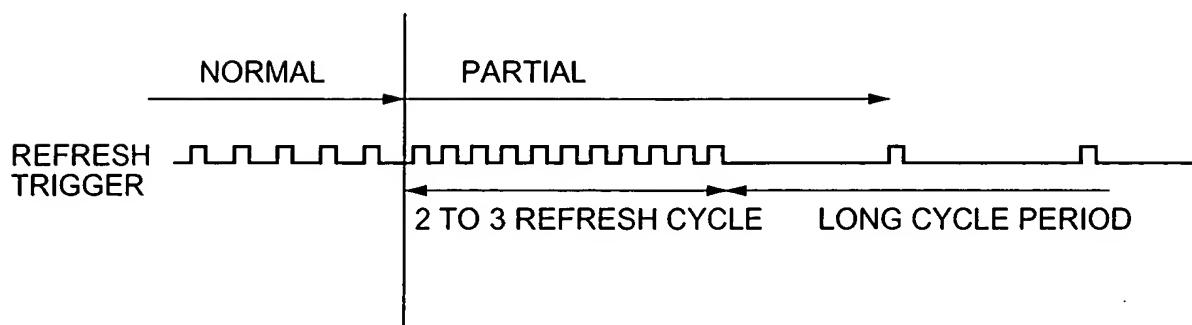


Fig. 6

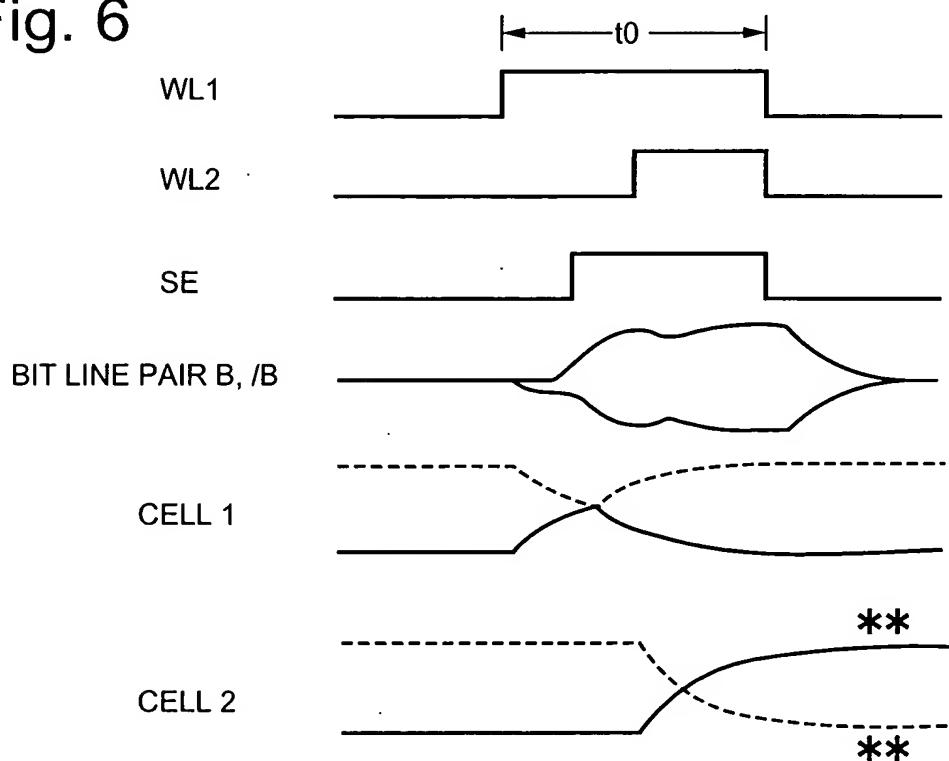


Fig. 7

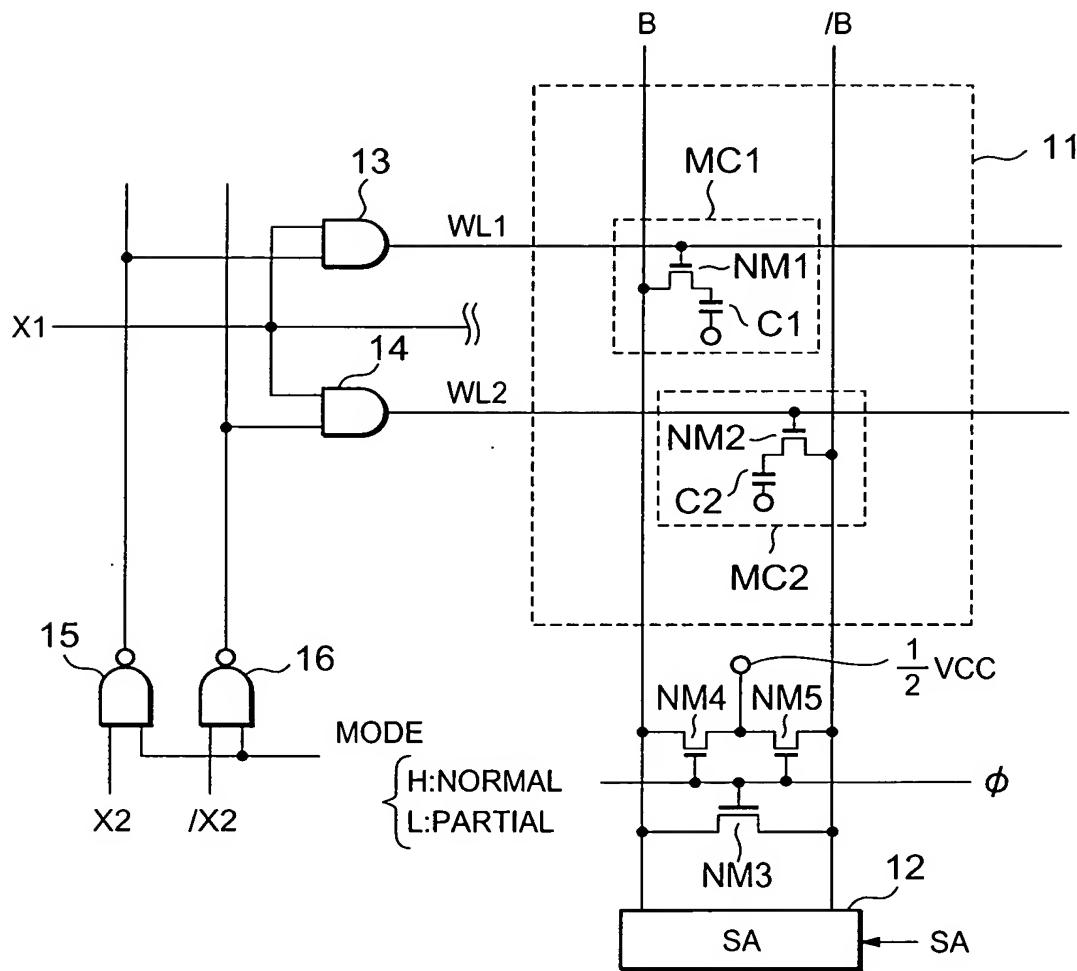


Fig. 8

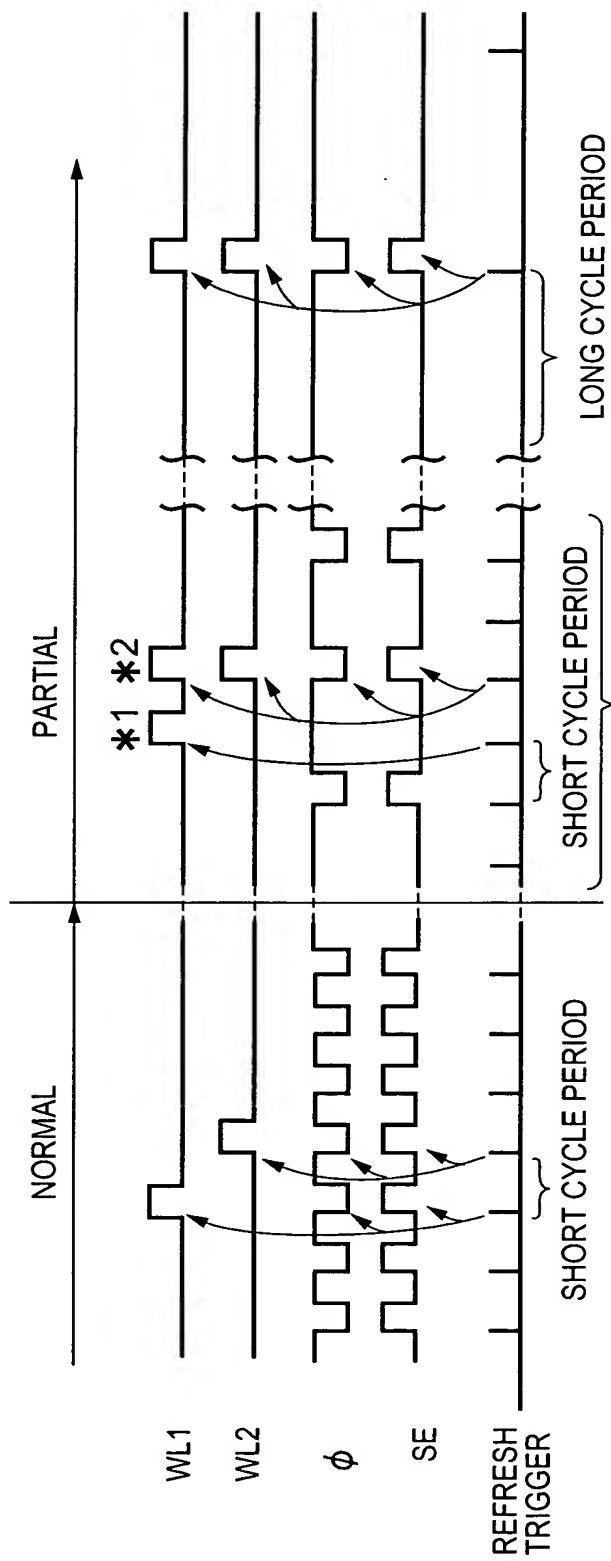


Fig. 9A

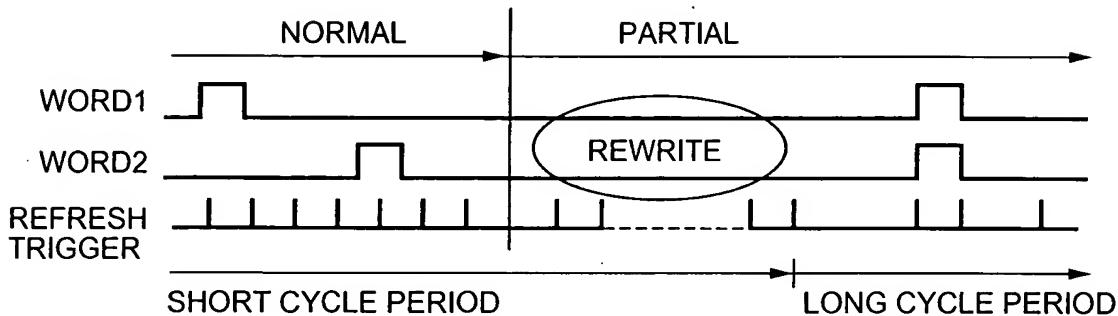


Fig. 9B

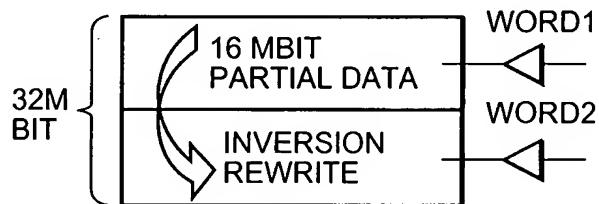


Fig. 9C

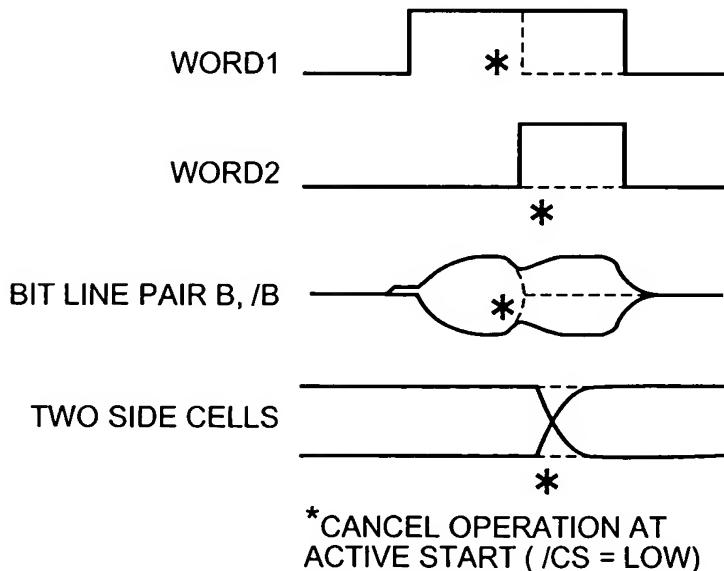


Fig. 10A

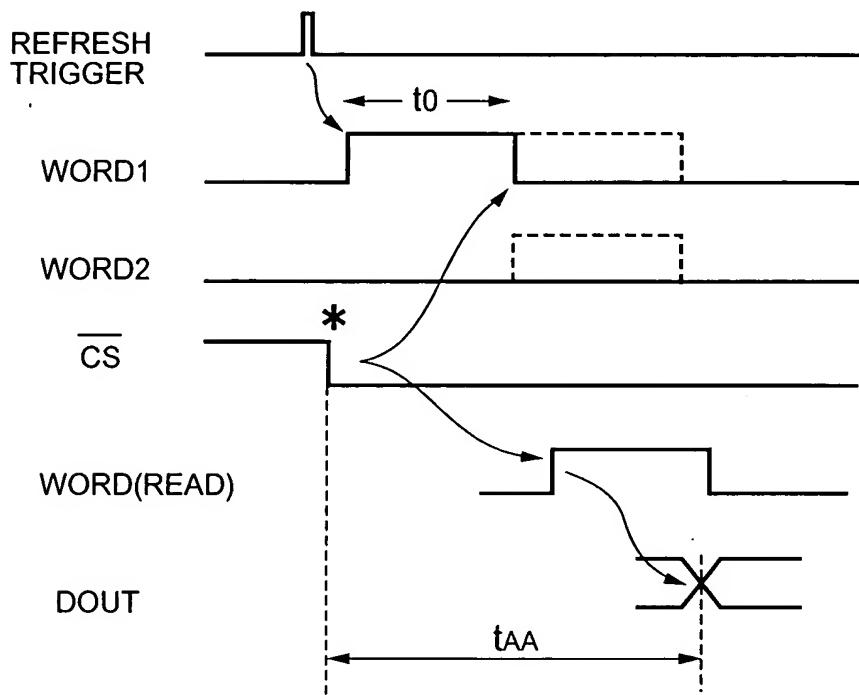


Fig. 10B

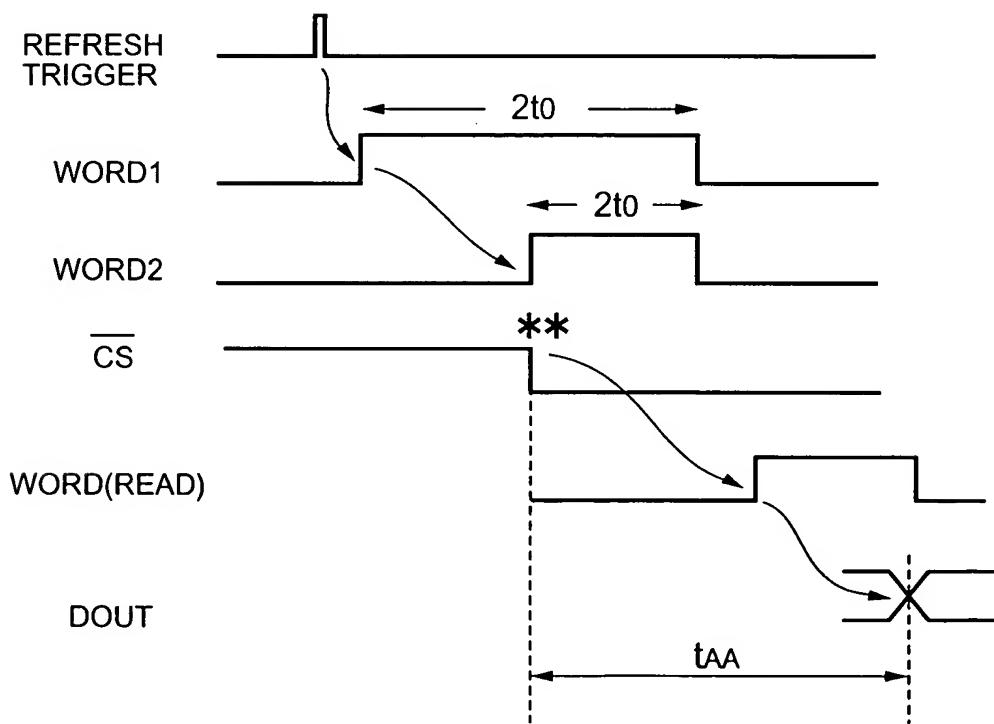
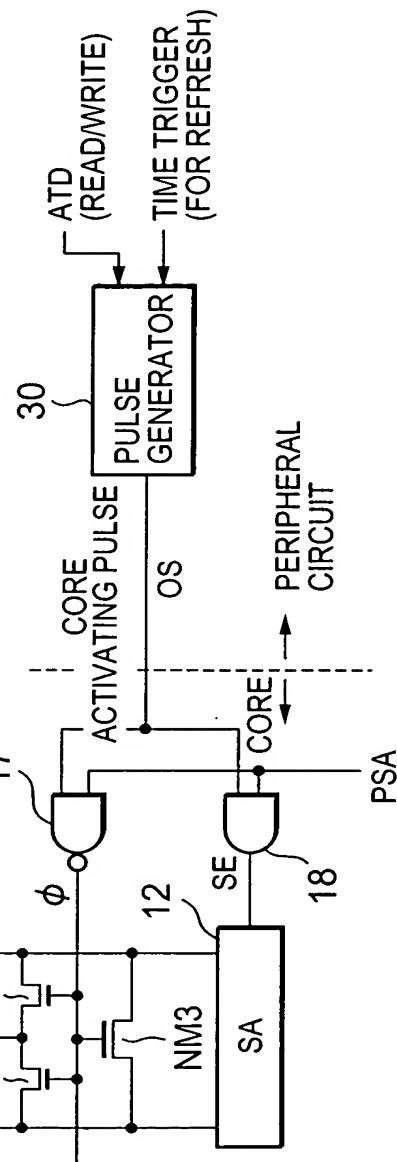
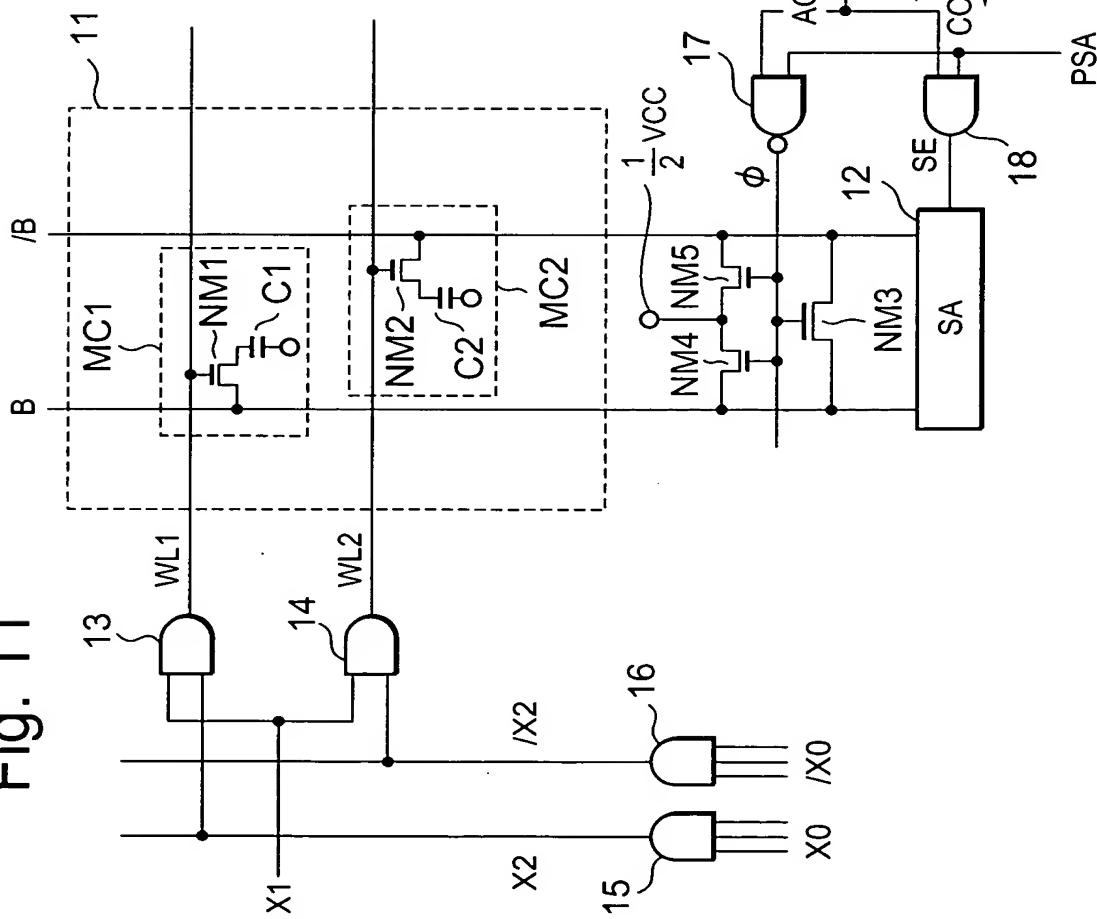


Fig. 11



Title: SEMICONDUCTOR MEMORY DEVICE HAVING
MODE STORING ONE BIT DATA IN TWO MEMORY CELLS
AND METHOD OF CONTROLLING SAME

Inventor: Hiroyuki TAKAHASHI

Filing Date: October 10, 2003; Serial No.: To be assigned

Sheet 9 of 11 Docket No.: NEM-05101

9 / 11

Fig. 12

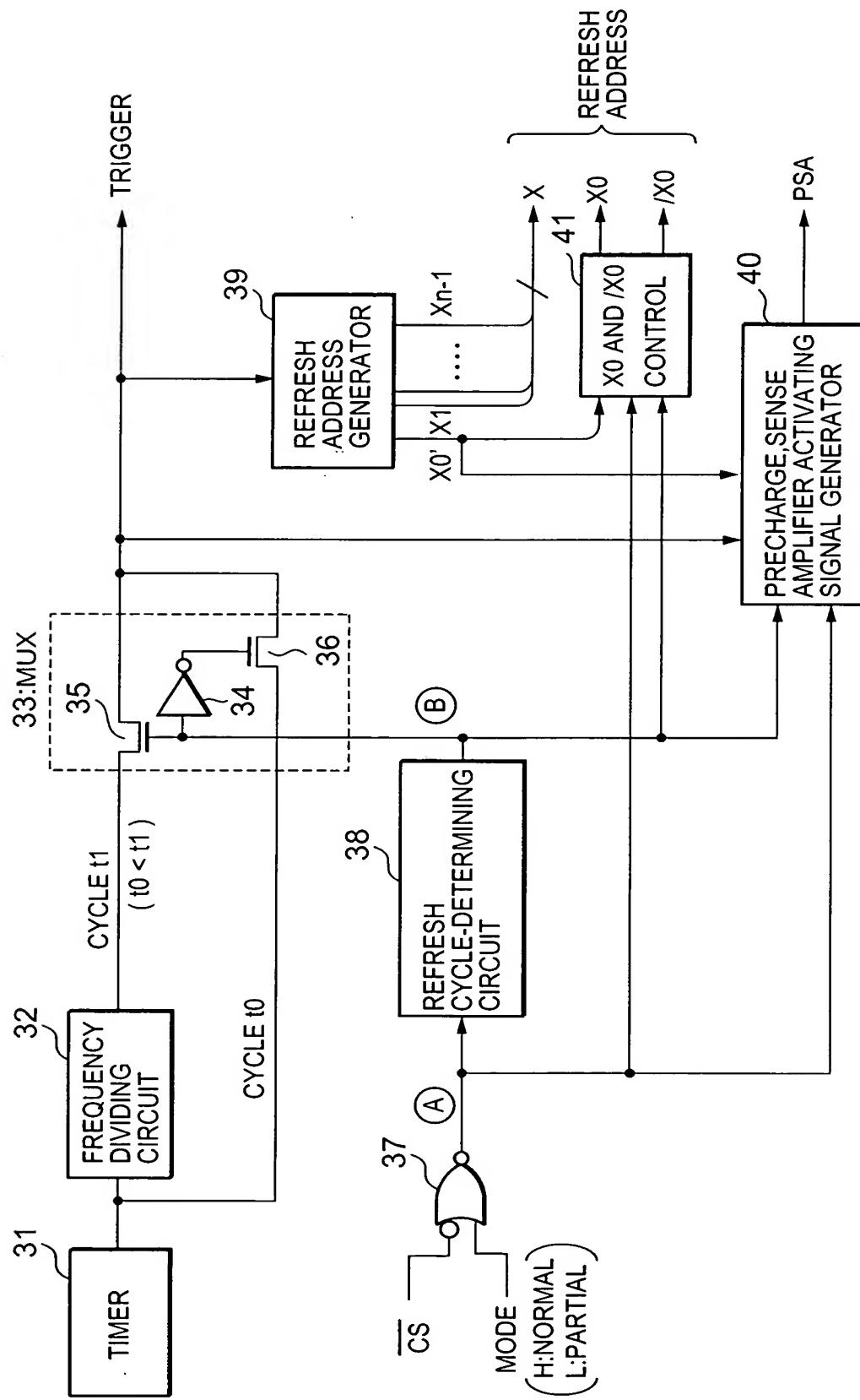


Fig. 13

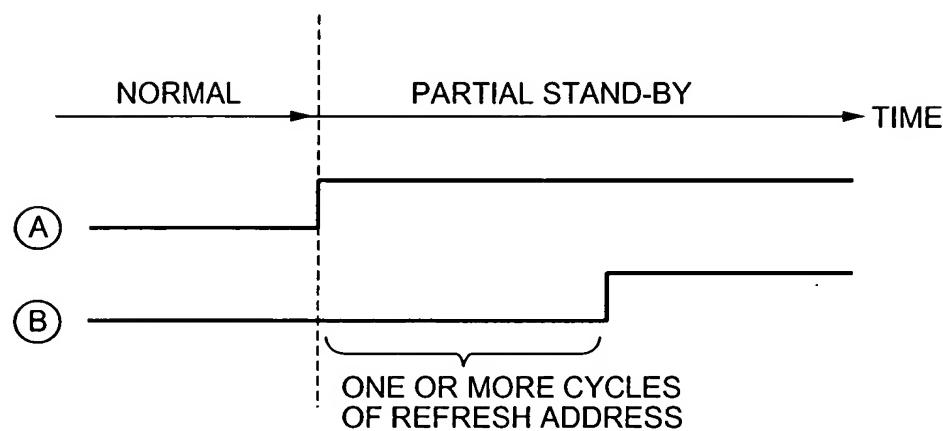


Fig. 14

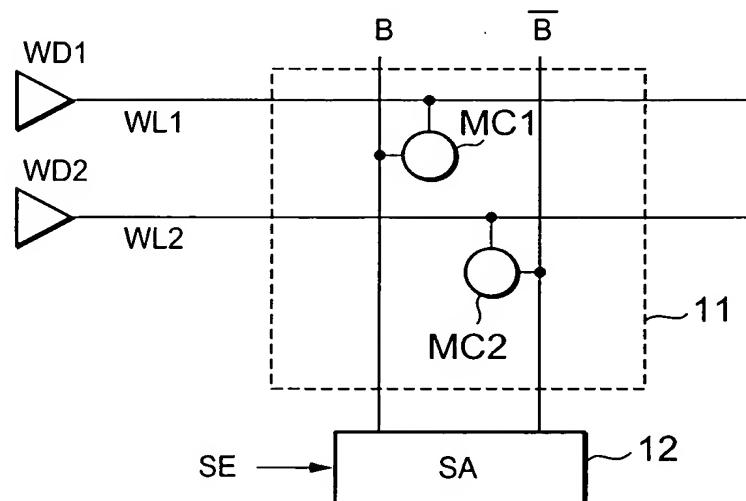


Fig. 15A

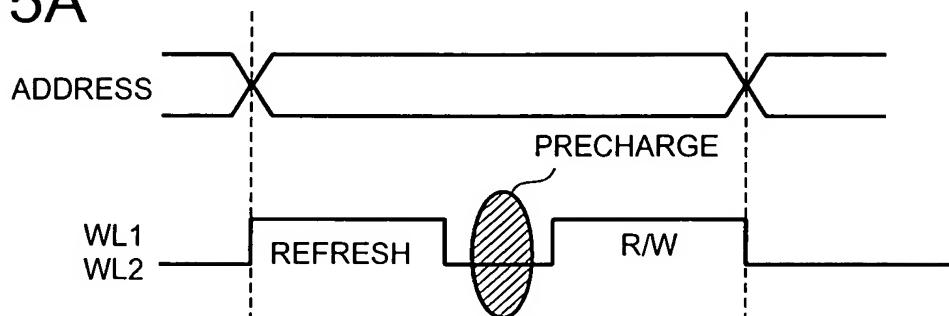


Fig. 15B

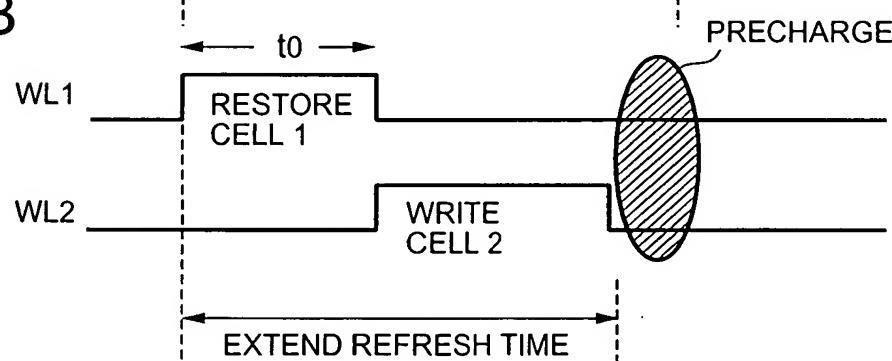


Fig. 16

